

FIG. 1 is a block diagram of a system for processing a video signal.

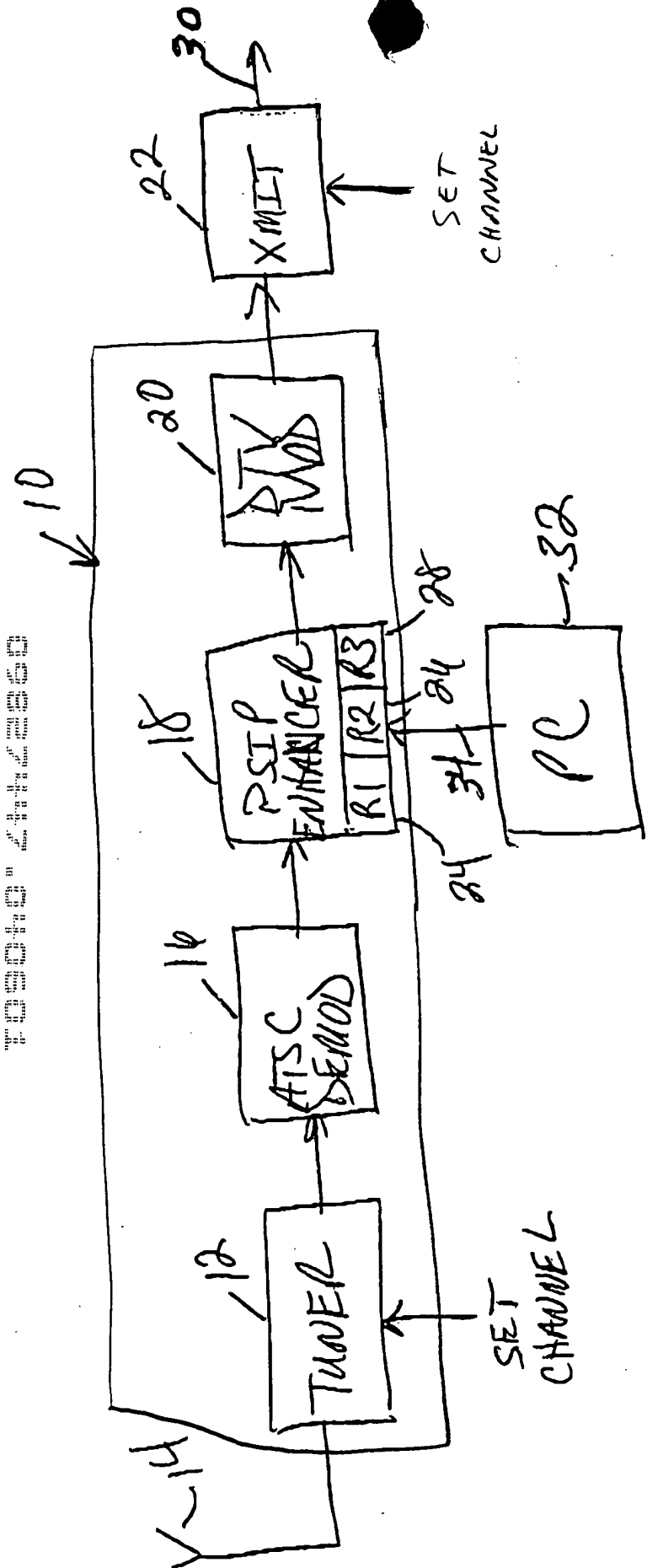


FIGURE 1

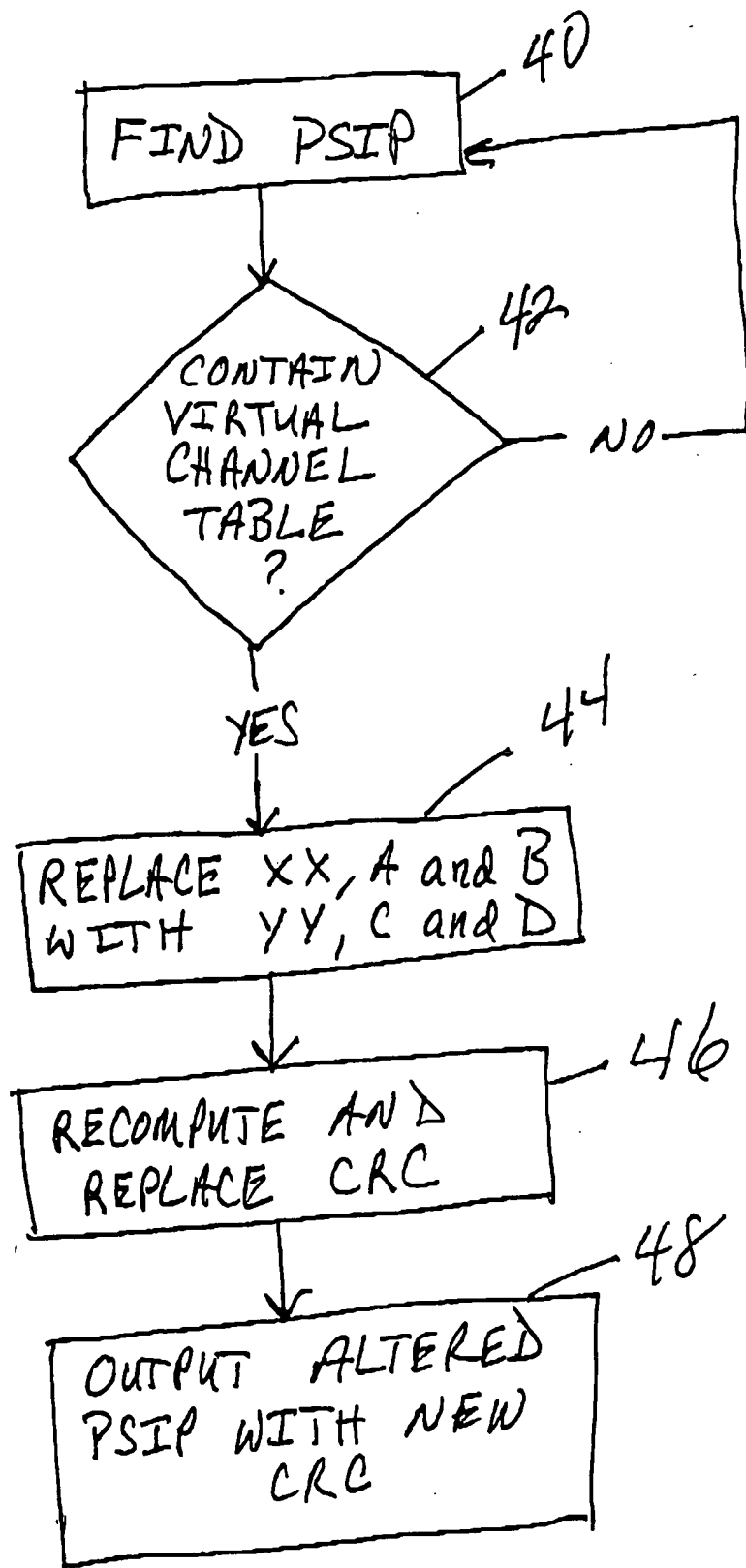


FIGURE  
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